

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

9/IPS
7/16/2
under

In re Application of:

Moon et al.

Serial No.: 09/874,631

Filed: June 5, 2001

For: FLEXIBLE BALL GRID ARRAY CHIP
SCALE PACKAGES AND METHODS OF
FABRICATION

Examiner: S. Clark

Group Art Unit: 2815

Attorney Docket No.: 4368US (99-0959)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

June 28, 2002
Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Joseph A. Walkowski
Typed/printed name of person whose signature is contained above

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 be considered by the Examiner and made of record. The listed documents are from co-pending applications Serial No. 10/150,653, filed May 17, 2002 and Serial No. 10/150,892, filed May 17, 2002. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as

defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56(b) exists.

DOCUMENTS

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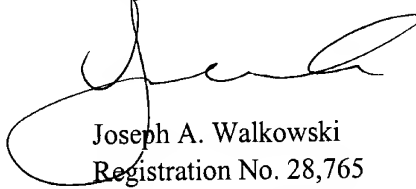
Other Documents

U. S. Patent Application entitled MICROELECTRONIC DEVICES AND METHODS OF MANUFACTURE, filed August 30, 2001, Serial No. 09/944,465.

Applicants offer to supply any explanation or discussion of the documents which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of the first Office Action on the merits. The fee pursuant to 37 C.F.R. § 1.17(p) is enclosed.

Respectfully submitted,



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JAW/hlg:dlm

Enclosures: Form PTO-1449
Copy of documents cited

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